

JCE322 U.S. PTO
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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM 10022224	FILING DATE 12/20/2001	CLASS 073	SUBCLASS 514.330	GAU 2856	EXAMINER <i>Kook</i> FLANLEY
**APPLICANTS: Katsumata Takashi; Higuchi Hirofumi;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-387619 12/20/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 11-076	
TITLE : Semiconductor device with shielding					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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